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PATENT

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v

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Pan et al.

Serial No.: 09/073,494

Filed: May 6, 1998

For: TECHNIQUE FOR ELIMINATION
OF PITTING ON SILICON SUBSTRATE
DURING GATE STACK ETCH

Examiner: To be assigned

Group Art Unit: 2811

Attorney Docket No.: 2915.1US
(96-149.1)



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(37 C.F.R. § 1.34(b))

Assistant Commissioner for Patents
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Sir:

In accordance with 37 C.F.R. § 1.34(b), please recognize the following individual as an associate agent/attorney herein in connection with the above-identified patent application:

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